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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	10/612293
Filing Date	June 30, 2003
First Named Inventor	Day, Daniel
Group Art Unit	2857
Examiner Name	Unknown

Sheet 1 of 1

Attorney Docket No: 884.879US1

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Examiner Initials *	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
me		"Boundary Scan (JTAG) Tools and Circuit Board Test Solutions", www.acculogic.com/Products/BoundaryScanHome.htm , 2 pages	
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DATE CONSIDERED

2/5/06

Substitute Disclosure Statement Form (PTO-1449)

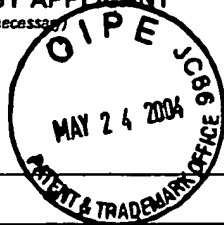
* EXAMINER: Initial if reference considered, whether or not in compliance with MPEP 609. Draw line through citation if not in compliance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached

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First Named Inventor Day, Daniel

Group Art Unit 2857

Examiner Name Unknown

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